

## AMENDMENTS TO THE CLAIMS

*Please amend the claims as follows:*

1. (Currently amended) A circuit comprising:  
a first device coupled with a first bus, ~~that is~~ wherein the first device is not  
compliant with a standard, the first device containing data;  
a second device coupled with a second bus, ~~that is~~ wherein the second device is  
compliant with the standard, the second device to be a temporary target for  
the data from the first device; and  
a memory to receive the data from the first device.
2. (Currently amended) The circuit of claim 1, further comprising a plurality of  
devices coupled with the second bus, ~~that are~~ wherein each of the plurality of  
devices is compliant with the standard, and wherein the plurality of devices  
includes the second device.
3. (Currently amended) The circuit of claim 2, further comprising a controller  
coupled with the first bus and the second bus to scan the plurality of standard  
devices to identify the second device.
4. (Original) The circuit of claim 1, wherein the second device comprises a function  
of a physical device.
5. (Original) The circuit of claim 1, wherein the first device comprises flash  
memory.

6. (Original) The circuit of claim 1, wherein the data comprises an operating system.
7. (Original) The circuit of claim 6, wherein the data includes a boot loader, the boot loader being stored as an option-ROM for the first device.
8. (Currently amended) The circuit of claim 1, wherein the standard comprises a PCI (peripheral component interconnect) specification, and wherein the second bus is a PCI bus.
9. (Currently amended) A method comprising:  
identifying a standard peripheral device that is coupled with a first bus to act as a temporary target for a non-standard peripheral device that is coupled with a second bus, the non-standard peripheral device containing data;  
assigning the standard peripheral device to the non-standard device; and  
dispatching the data to memory.
10. (Currently amended) The method of claim 9, wherein identifying the standard peripheral device comprises choosing the standard peripheral device from a plurality of standard peripheral devices that are coupled with the first bus.
11. (Original) The method of claim 10, wherein choosing the standard peripheral device comprises pre-selecting the standard peripheral device before commencing operations.

12. (Currently amended) The method of claim 10, wherein choosing the standard peripheral device comprises scanning the plurality of standard peripheral devices coupled with the first bus to identify a suitable device.
13. (Original) The method of claim 9, wherein standard the standard comprises a PCI (peripheral component interconnect) specification.
14. (Original) The method of claim 9, wherein the data comprises an operating system.
15. (Original) The method of claim 14, wherein the data includes a boot loader, the boot loader being stored as an option-ROM.
16. (Currently amended) A computer system comprising:  
a processor;  
a first bus, the first bus being in compliance with a standard;  
a first device that is not compliant with a standard, the first device being coupled with a second bus, the first device containing data;  
a plurality of devices in compliance with the standard, each of the plurality of devices being coupled with the second bus, the plurality of devices including a second device to be assigned as a temporary target for the data from the first device; and  
a memory to receive the data from the first device.
17. (Original) The computer system of claim 16, wherein the computer system is an embedded system.

18. (Currently amended) The computer system of claim 16, further comprising a controller coupled with the first bus and the second bus to scan the plurality of devices in compliance with the standard to identify the second device.
19. (Original) The computer system of claim 16, wherein the plurality of devices includes one or more functions of a physical device.
20. (Original) The computer system of claim 16, wherein the first device comprises flash memory.
21. (Original) The computer system of claim 16, wherein the data comprises an operating system.
22. (Original) The computer system of claim 16, wherein a portion of the data is stored as an option-ROM for the non-standard device.
23. (Original) The computer system of claim 16, wherein the standard comprises a PCI (peripheral component interconnect) specification.
24. (Currently amended) A machine-readable medium having stored thereon data representing sequences of instructions that, when executed by a processor, cause the processor to perform operations comprising:  
  
identifying a standard peripheral device on a first bus to act as a temporary target  
  
for a non-standard peripheral device on a second bus, the non-standard peripheral device containing data;  
  
assigning the standard peripheral device to the non-standard device; and  
  
dispatching the data to memory.

25. (Currently amended) The medium of claim 24, wherein identifying the standard peripheral device comprises choosing the standard peripheral device from a plurality of standard peripheral devices on the first bus.
26. (Original) The medium of claim 25, wherein choosing the standard peripheral device comprises pre-selecting the standard peripheral device before commencing operations.
27. (Currently amended) The medium of claim 25, wherein choosing the standard peripheral device comprises scanning the plurality of standard peripheral devices on the first bus to identify a suitable device.
28. (Original) The medium of claim 24, wherein standard the standard comprises a PCI (peripheral component interconnect) specification.
29. (Original) The medium of claim 24, wherein the data comprises an operating system.
30. (Original) The medium of claim 29, wherein the data includes a boot loader, the boot loader being stored as an option-ROM.

*Please add the following new claims:*

31. (New) The apparatus of claim 3, further comprising a second controller coupled with the controller and the memory, wherein the memory receives the data via the second controller.

32. (New) The method of claim 12, wherein the scanning of the plurality of standard peripheral devices is performed by a first controller that is coupled with the first bus and the second bus.
33. (New) The method of claim 32, wherein the dispatching of the data to memory comprises transferring the data to memory via a second controller that is coupled with the memory and the first controller.
34. (New) The computer system of claim 18, further comprising a second controller coupled with the controller and the memory, wherein the memory receives the data via the second controller.
35. (New) The medium of claim 25, wherein the choice of the standard peripheral devices from the plurality of standard peripheral devices is performed by a first controller that is coupled with the first bus and the second bus.
36. (New) The method of claim 35, wherein the dispatching of the data to memory comprises transferring the data to memory via a second controller that is coupled with the memory and the first controller.